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  *         U. S.   P A T E N T   T E X T   F I L E               *
  *                                                               *
  *  THE WEEKLY PATENT TEXT AND IMAGE DATA IS CURRENT             *
  *  THROUGH July 06, 1999                          *             *
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=> s combin? (p) video (p) non-video

       1257629 COMBIN?
         99810 VIDEO
        912750 NON
         99810 VIDEO
           306 NON-VIDEO
                 (NON(W)VIDEO)
L1          31 COMBIN? (P) VIDEO (P) NON-VIDEO

=> d kwic 1-31

TEXT DATA FOR PATENT 5,912,700 IS NOT AVAILABLE, SEE IMAGE DATA, THE
MICROFILE OR PAPER INSTEAD

US PAT NO:      5,884,067 [IMAGE AVAILABLE]              L1: 2 of 31

CLAIMS:

CLMS(3)

 3. A memory controller, comprising:
 a planar data packer to receive a plurality of unpacked video data
  from a memory device, each of the unpacked video data having at
  least two channels of information, and to pack the unpacked video
  data into packed video data by stripping at least one channel from
  each of the plurality of unpacked video data and then combining
  any remaining unstripped channel data, prior to sending the packed
  video data to a bus master;
 a planar data unpacker to receive the packed video data from the bus
  master, the packed video data having at least one missing channel
  of the at least two channels of the unpacked video data, the planar
  data unpacker then unpacking the packed video data to unpacked
  video data for storage in the memory device; and
 an error correction code (ECC) generator to generate ECC information to
  be appended to non-video data received from the bus master
  prior to sending the non-video data to the memory device, and
  to generate check ECC data to be compared with the ECC information
  appended to the non-video data received from the memory device
  prior to sending the non-video data to the bus master.

CLAIMS:

CLMS(7)

 7. A memory controller method, comprising the steps of:
 planar data packing by receiving a plurality of unpacked video data
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from a memory device each of the unpacked **video** data having at least two channels of information, and packing the unpacked **video** data into packed **video** data by stripping at least one channel from each of the plurality of unpacked **video** data and then **combining** any remaining unstripped channel data, prior to sending the packed **video** data to a bus master;

planar data unpacking by receiving the packed **video** data from the bus master, the packed **video** data having at least one missing channel of the at least two channels of the unpacked **video** data, and then unpacking the packed **video** data to unpacked **video** data for storage in the memory device, thereby providing **video** data translation;

generating error correction code (ECC) information to be appended to **non-video** data received from the bus master prior to sending the **non-video** data to the memory device; and

generating check ECC data to be compared with the ECC information appended to the **non-video** data received from the memory device prior to sending the **non-video** data to the bus master.

US PAT NO: 5,856,973 [IMAGE AVAILABLE] L1: 3 of 31

DETDESC:

DETD(45)

To reiterate, in each of the above embodiments, the private application data was used to generate a **video** signal to be **combined** with the decoded MPEG **video**. However, other types of private application data, such as **non-video** data for example, can be processed by private application processor(s) at the far end of the communications link